

<b>Pin No.</b>	<b>Pin Name</b>	<b>Wire Color</b>	<b>Destination</b>
1	+15'	BR	JK-+15' (C1-5)
2	HR	RE	JK-HR (C1-2)
3	HL	WH	JK-HL (C1-1)
4	E	BL	JK-E (C1-3)
5	E	S BR S	
6	OUT	S BR	JK-OUT (C1-7)
7	FC	GR	JK-FC (C1-4)

### C2

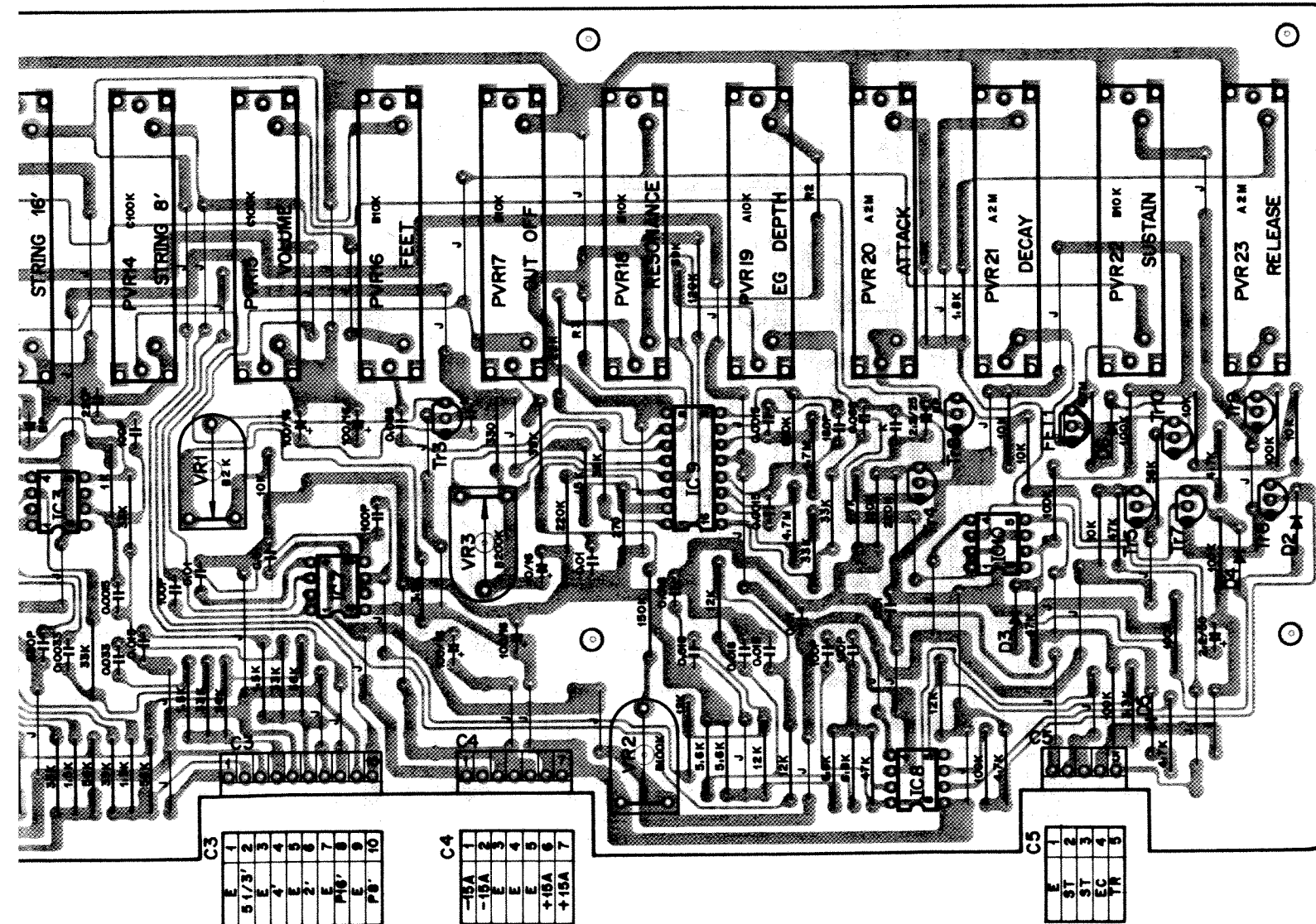
Pin No.	Pin Name	Wire Color	Destination
1	VTR	BE	DM-TR (C7-2)
2	S	VI	TE-S (C3-1)
3	TE	GY	TE-T/E (C3-2)
4	IC	WH	DM-IC (C4-2)
5	IC	WH	CPA-IC (C4-1)
6	NC	—	—
7	VM	GG	DM-VM (C4-1)

C3			
Pin No.	Pin Name	Wire Color	Destination
1	-15A	YE	CPB- -15A (C4-2)
2	-15A	—	—
3	E	BL	CPB-E (C4-5)
4	E	—	—
5	+15A	BR	CPB+15A (C4-7)
6	+15A	—	—
7	E	S GG S	—
8	TE <del>O</del>	S GG S	TE-1 (C2-4)
9	E	S SB S	—
10	TE1	S SB	TE-O (C3-3)

Pin No.	Pin Name	Wire Color	Destination
1	IC	WH	CPA-IC (C2-5)
2	OR	S RE	CPA-OBO (C6-8)
3	E	S RE S	
4	ST	S OR	CPA-PBO (C6-2)
5	E	S OR S	
6	PS	S YE	CPA-PS (C1-3)
7	E	S YE S	CPB-PS (C1-8)

Pin No.	Pin Name	Wire Color	Destination
1	-15D	YE	DC-15 (C1-2)
2	-15D	—	—
3	VSS	BL	DC-E (C2-8)
4	VSS	—	—
5	SO	S GR	DM-Si (C4-7)
6	VSS	S GR S	—
7	ΦM	S BE	DM-ΦM (C4-3)
8	VSS	S BE S	—
9	SY	S VI	DM-SY (C4-5)
10	VSS	S VI S	—

### CPB Circuit Diagram



C1

Pin No.	Pin Name	Wire Color	Destination
1	EC	BL	DC-E (C2-9)
2	-15C	YE	DC-15 (C2-2)
3	TR	BE	DM-TR (C7-3)
4	TR	BE	CPB-TR (C5-5)
5	E	\$ GR \$	
6	2 2/3	\$ GR \$	DM-2 2/3 (C6-2)
7	E	\$ YE \$	
8	PS	\$ YE \$	CPA-PS (C4-6)

**C5**

Pin No.	Pin Name	Wire Color	Destination
1	E	S GR S	
2	ST	S GR	CPA-PB1 (C6-4)
3	ST	—	—
4	EC	BL	DC-E (C2-10)
5	TR	BE	CPB-TR (C1-4)

**C2**

Pin No.	Pin Name	Wire Color	Destination
1	E	S YES	
2	OO	S YE	CPA-OB1 (C6-6)
3	E	S WH S	
4	ENC	S WH	DM-ENC (C5-6)
5	E	S BR S	
6	16'	S BR	DM-16' (C5-4)
7	E	S RE S	
8	8'	S RE	DM-8' (C5-6)

**C3**

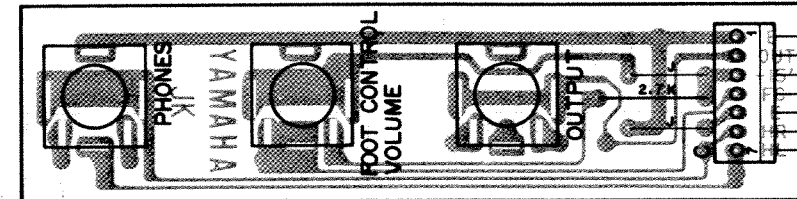
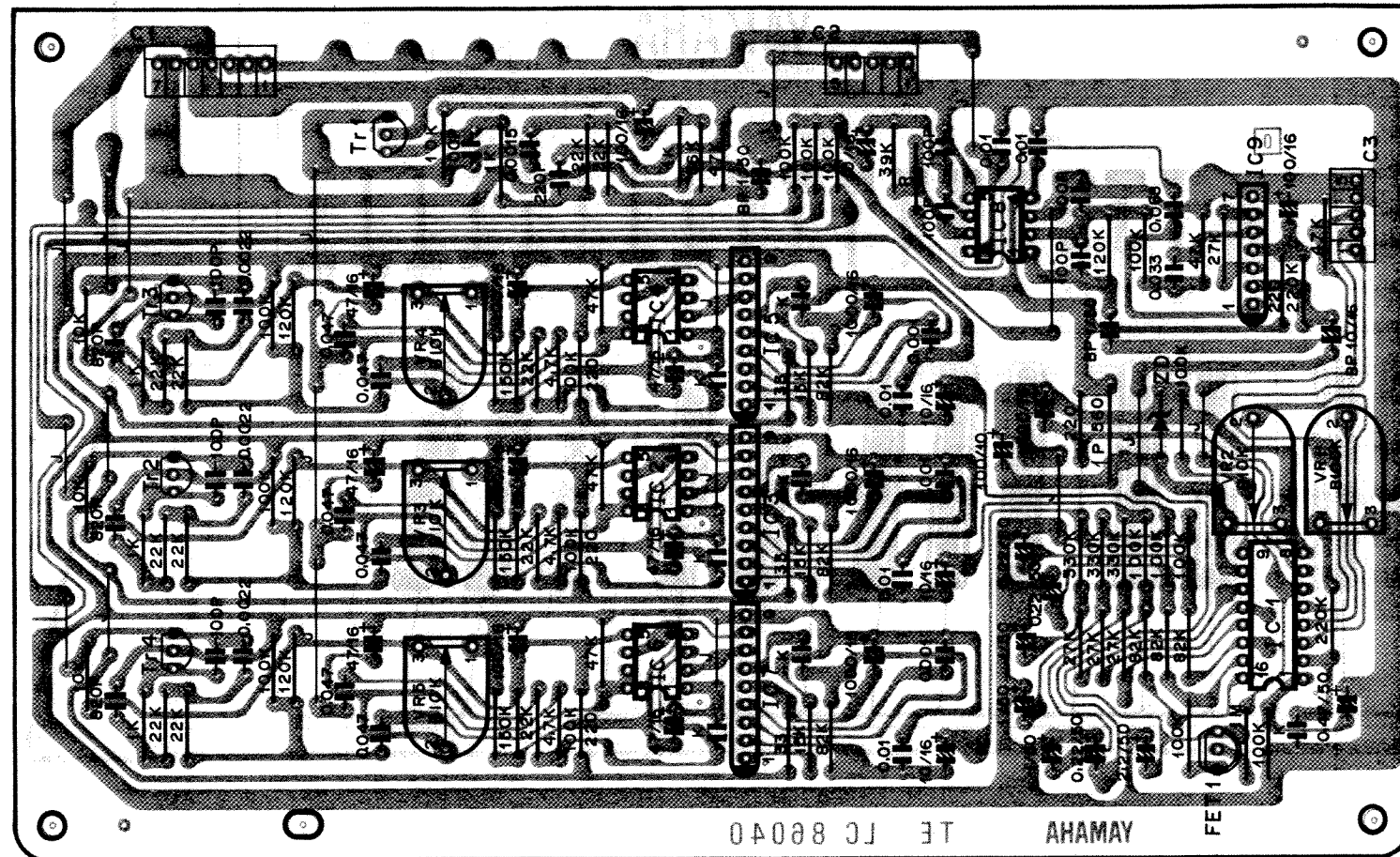
Pin No.	Pin Name	Wire Color	Destination
1	E	S O R S	
2	5 1/3'	S O R	DM-5 1/3' (C5-2)
3	E	S Y E S	
4	4'	S Y E	DM-4' (C6-8)
5	E	S B E S	
6	2'	S B E	DM-2' (C6-4)
7	E	S V I S	
8	P16'	S V I	DM-P16' (C8-1)
9	E	S G Y S	
10	P8'	S G Y	DM-P8' (C8-4)

**C4**

Pin No.	Pin Name	Wire Color	Destination
1	-15A	YE	DC- 15 (C1-4)
2	-15A	YE	CPA- 15A (C3-5)
3	E	BL	DC-E (C2-6)
4	E	BL	DC-E (C2-7)
5	E	BL	CPA-E (C3-3)
6	+15A	BR	DC-+15 (C1-6)
7	+15A	BR	CPA-+15A (C3-5)

## Notes)

1. Circuit Board : LC28942
2. IC  
IC 1 ~ 4, 6 ~ 8, : NJM4558DV  
10
- IC 5 : iG02601
- IC 9 : iG00156
3. Transistors  
Tr 1 ~ 7, 9 : 2SC1815(O) (Y)  
Tr 8, 10 : 2SA1015(O) (Y)
4. FET  
FET 1 : 2SK30A(Y)
5. Diodes  
D1 ~ 6 : 1S1555
6. Capacitor  
(K) marked : Ceramic Capacitor 1000P

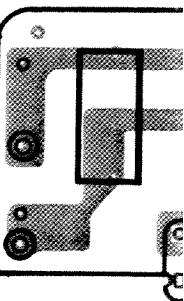
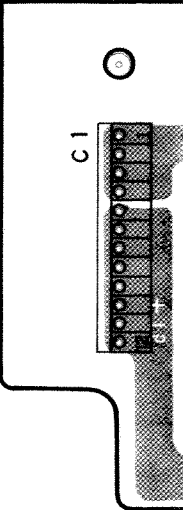


C	
1	HL
2	HR
3	E
4	FC
5	+15'
6	OUT
7	E

Notes)

1. Circuit Board : LC 28942

C			
Pin No.	Pin Name	Wire Color	Destination
1	HL	WH	CPA-HL (C1-3)
2	HR	RE	CPA-HR (C1-2)
3	E	BL	CPA-E (C1-4)
4	FC	GR	CPA-FC (C1-7)
5	+15'	BR	CPA+15' (C1-1)
6	OUT	SBR	CPA-OUT (C1-6)
7	E	SBR S	



C1

Pin No.	Pin Name	Wire Color	Destination
1	+15	BR	DC+15 (C1-8)
2	+15	—	—
3	+15	—	—
4	E	BL	DC-E (C2-11)
5	E	—	—
6	-15	—	—
7	-15	YE	DC-15 (C2-1)

C3

Pin No.	Pin Name	Wire Color	Destination
1	S	VI	CPA-S (C2-2)
2	T/E	GY	CPA-TE (C2-3)
3	O	S SB	CPA-TEI (C3-10)
4	E	—	—
5	E	S SB S	—

C2

Pin No.	Pin Name	Wire Color	Destination
1	E	S GG S	—
2	E	—	—
3	E	—	—
4	I	S GG	CPA-TEO (C3-8)
5	I	—	—

Notes)

1. Circuit Board : LC86041

2. IC

IC 1 : YM63300

IC 2, 4, 6 : MN3009

IC 3, 5, 7 : iG03290

IC 8 : NJM4558DV

IC 9 : iG02590

3. Transistors

Tr 1 ~ 4 : 2SC1815(O) (Y)

4. FET

FET 1 : 2SK105F

5. Diode

ZD : WZ050

6. Capacitor

K marked : Ceramic Capacitor 1000P

7. IC9 (iG02660, iG02590)

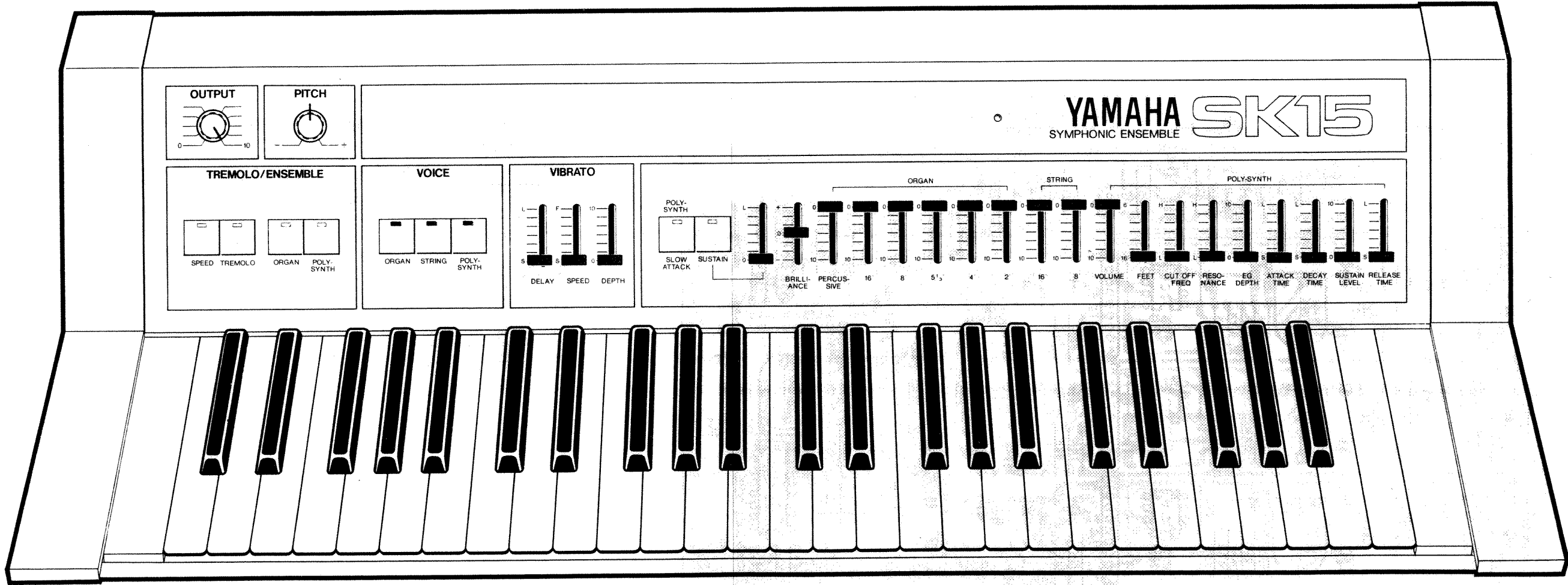
Rank	K	L	M
R	56K	47K	39K

C1

Pin No.	Pin Name	Wire Color	Destination
1	-15	YE	DM-15
2	-15	YE	CPA-15
3	-15	YE	DM-15
4	-15	YE	CPB-15
5	+15	BR	DM+15D
6	+15	BR	CPB+15A
7	+15	BR	DM+15A
8	+15	BR	TE+15 (C
9	+15	—	—
10	+15	—	—
11	+15	—	—
12	+15	—	—



PANEL SETTING



Item	Setting	Test point	Adjustment & reading	Where to adjustment	Remark
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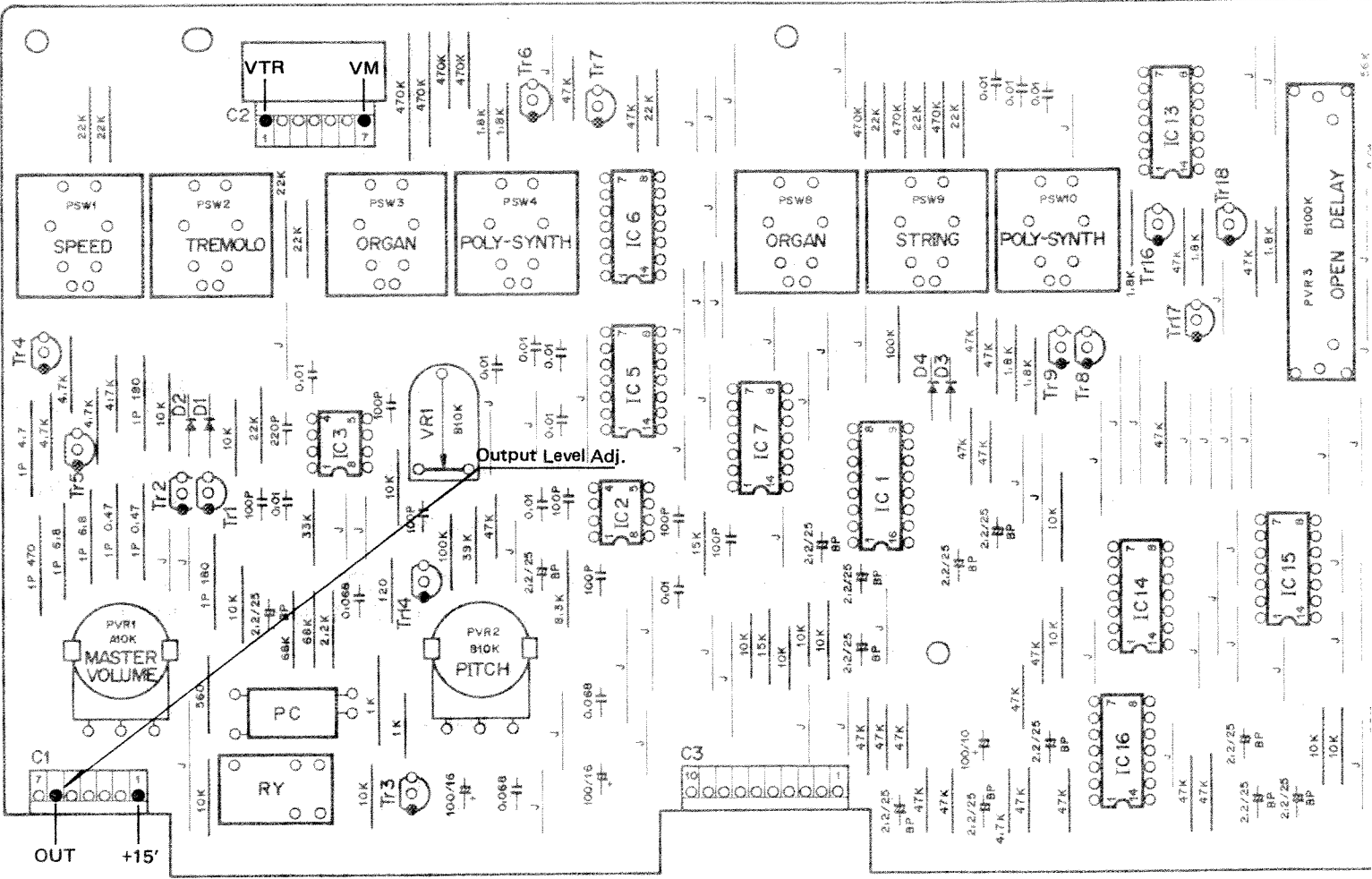
  

Condition	PSC terminal levels		Sound volume envelope of sound source
	P10	P9	
SLOW ATTACK OFF SUSTAIN	L	H	
SLOW ATTACK ON SUSTAIN	H	H	
SLOW ATTACK ON SUSTAIN	L	L	
SLOW ATTACK ON SUSTAIN	H	L	

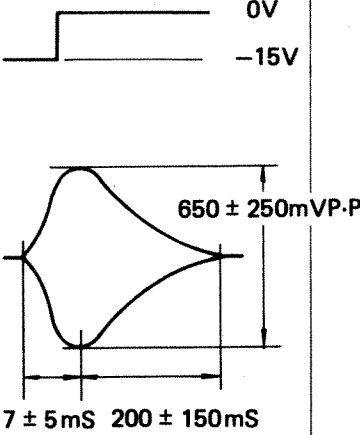

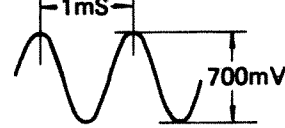
  

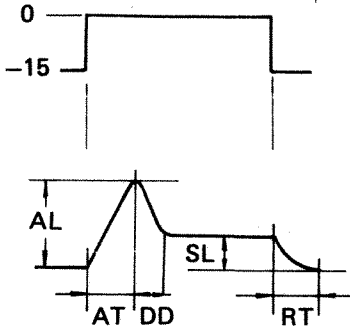
● SUSTAIN TIME DATA

SUSTAIN Lever (PSW11)	Sustain data			Sustain time(msec)
	D <sub>5</sub> (S <sub>1</sub> )	D <sub>6</sub> (S <sub>2</sub> )	D <sub>7</sub> (S <sub>3</sub> )	
LONG	8	L	L	1600
	7	H	L	1100
	6	H	H	750
	5	L	H	450
	4	L	H	250
	3	H	H	150
	2	H	L	90
SHORT	1	L	H	30



CPB circuit board

Item	Setting	Test point	Adjustment & reading	Where to adjustment	Remark
PERCUSSIVE 2 2/3'	Depress any key  PERCUSSIVE (PVR7) ..... 10 Depress the C5 key	TR (C1-3)  OO terminal (C2-2)	 0V -15V 650 ± 250mV·P 7 ± 5mS 200 ± 150mS		Check  Check
POLY-SYNTH Filter circuit	FEET LEVER (PVR16) ..... 16				
Peak Point	CUT OFF FREQ (PVR17)	pin 2 TP34	+5V ± 0.2V	PVR17 set	Check
Peak level adjustment (Tone quality adjustment)	RESONANCE (PVR18)	pin 2 TP35	+5V ± 0.2V	PVR18 set	Check
	POLY-SYNTH VOLUME (PVR15) ..... 10 Depress the C5 key	PS (C1-8)	Peak Point  Max.  Peak level  1mS 700mV	VR2 (B100K)  VR3 (B200K)	Adjustment  Adjustment
+10V adjustment		Tr3 Emitter	+10 ± 0.2V	VR1 (B2K)	Adjustment

Item	Setting	Test point	Adjustment & reading	Where to adjustment	Remark
EG-waveform check	Depress any key	TR (C5-5)  IC 10½ pin 1	 0 -15 AL AT DD SL RT		Check
		ATTACK TIME	5 ± 3mS ~ 3S	PVR20	Check
		DECAY TIME	7 ± 4mS ~ 10 ± 3S	PVR21	Check
		SUSTAIN LEVEL	0 ~ 1V	PVR22	Check
		RELEASE TIME	7 ± 4mS ~ 10 ± 3S	PVR23	Check
		ATTACK LEVEL	10 ± 1V (Usually)		Check